Abstract: In recent years, power demand of industrial applications has increased significantly reaching some megawatts. The use of multilevel converters for applications of medium and high powers is proposed as a solution to drawback semiconductor technology. A multilevel converter not only achieves high power ratings, but also enables the use of renewable energy sources. Renewable energy sources such as photovoltaic can be easily interfaced to a multilevel converter system for a high power application. This paper presents the simulation study in Matlab/Simulink of a grid connected photovoltaic three phase Neutral Point Clamped (NPC) inverter with DC/DC boost converter for constant and variable solar radiation.

Keywords: Grid-connected PV systems, Multi level inverter, Multiple low frequency PWM, LCL filter, MPPT controller.

1 Introduction

Renewable energy sources such as photovoltaic, wind, and fuel cells can be easily interfaced to a multilevel converter system for a high power application [1 – 4]. The concept of multilevel converters has been introduced since 1975 [5]. Capacitors, batteries, and renewable energy voltage sources can be used as multiple dc voltage sources. The commutation of the power switches aggregate these multiple dc sources in order to achieve high voltage at the output; however, the rated voltage of the power semiconductor switches depends only on the rating of the dc voltage sources to which they are connected. Among these converters, the three phase three levels NPC inverters used in photovoltaic systems connected to the grid are mentioned. The simulation study in Matlab/Simulink of a grid connected photovoltaic three phase NPC inverter with DC/DC boost converter has been presented in this paper. A photovoltaic generator of \( N_p \) solar modules branches in parallel (\( N_p = 10 \)) containing \( N_S \) modules in series (\( N_S = 19 \)) is used. Each parallel branch is connected to an inverter via a DC/DC boost converter. This converter is controlled by a current generated by a controller based on a maximum power point tracking (MPPT)
algorithm known as the Perturbation and Observation (P&O). The inverter control is based on a multiple low frequency dual reference PWM strategy accompanied by a PID controller. The study has been done for a constant and a variable solar radiation and the obtained results are discussed.

2 Grid Connected Photovoltaic System Structure

The global system, shown in Fig. 1, consists of a Photovoltaic generator (PVG), two identical input capacitors $C$ and $C'$ to form a capacitive divider, an inverter composed of 12 switches with six diodes connected to the neutral point, an output filter (LCL) and a DC/DC boost converter. The whole system is connected to the grid.

![Photovoltaic Global Grid Connected System](image)

**Fig. 1** – Photovoltaic global grid connected system.

2.1 PVG System model

Many models are developed to represent photovoltaic generator (PVG) $I$-$V$ characteristic [6]. In this work the one diode model, whose diagram is shown in Fig. 2, is chosen. The output current $I$ of solar cell is given by (1)

$$I = I_L - I_D,$$  \hspace{1cm} (1)

where $I_L$ is the photocurrent and $I_D$ is the diode current. The photocurrent $I_L$ varies with the irradiance and temperature, it is determined with respect to the values given in the standard test conditions (STC):

$$I_L = \left( \frac{G}{G_{ref}} \right) I_{SC_{ref}} \left[ 1 + K_0 \left( T - T_{ref} \right) \right],$$  \hspace{1cm} (2)
where: $G_{ref}$ is the reference value of the irradiation ($G_{ref} = 1000 \text{ W/m}^2$), $T_{ref}$ the reference value of the temperature ($T_{ref} = 25^\circ \text{C}$) and $I_{sc \_ref}$ the short circuit current at the STC.

$T$ and $G$ are respectively temperature and irradiating operation of the cell.

![One diode circuit of the PV model.](image)

$K_0$ is the temperature coefficient of the current which is given by the following equation:

$$rK_0 = \frac{I_{sc \_T} - I_{sc \_ref}}{T - T_{ref}}, \quad (3)$$

where $I_{sc \_T}$ is the current of the PV cell at the operating temperature and $I_{sc \_ref}$ the current of the PV cell at the reference temperature $T_{ref}$.

The diode current is given by:

$$I_D = I_0 \left[ \left( \exp \left( \frac{q}{nkT} \left( V + R_s I \right) \right) - 1 \right) \right], \quad (4)$$

where $k$ is the Boltzmann’s constant ($k = 1.38065 \times 10^{-23} \text{ J/K}$), $q$ the electrical charge of the electron with a value equal to $1.60217733 \times 10^{-19} \text{ C}$, $n$ is the quality factor which varies typically from 1 to 2, $R_s$ is the series resistance of the PV module and $I_0$.

The reverse bias saturation current which varies with temperature “$T$” according to the following expression:

$$I_0 = I_{0 \_T_{ref}} \left( \frac{T}{T_{ref}} \right)^{\frac{3}{n}} \exp \left( \frac{-qV_g}{nk} \left( \frac{1}{T} - \frac{1}{T_{ref}} \right) \right), \quad (5)$$

where $V_g$ is the Band gap voltage (for the Silicon $V_g = 1.11 \text{ V}$) and $I_{0 \_T_{ref}}$ the reverse bias saturation current at the reference temperature.

The saturation current of the diode is given by:
where $V_{oc}$ is the open-circuit voltage of the PV cell, $I_{SC_{-ref}}$ and $V_{OC_{-ref}}$ are respectively the short circuit current and the open circuit voltage at the STC. A series resistance $R_S$ was included:

$$R_S = \left(-\frac{dV}{dI}\right)_{Voc} - \frac{1}{X_V},$$

where:

$$X_V = I_{0_{-ref}} \frac{q}{nkT_{ref}} \exp\left(q \frac{V_{OC_{-ref}}}{nkT_{ref}} \right).$$

The shunt resistance $R_{Sh}$ is neglected.

The overall equation for modeling the cell is:

$$I = I_L - I_0 \left[\exp\left(q \frac{V + R_S I}{nkT} \right) - 1\right],$$

where $V$ and $I$ are the output voltage and the output current of the cell. $I_L$ is the photocurrent, $q$, $n$, $k$ are already defined.

In this work a PVG, which consists of $N_P$ branches of $N_S$ PV modules in each branch, is used:

$$V_{PVG} = N_S \times V_{Panel},$$

$$I_{PVG} = N_P \times I_{Panel}.$$
2.2 DC/DC boost converter and its control

2.2.1 DC/DC boost converter model

The DC/DC boost converter model is given in Fig. 4.

![Boost converter schematic.](image)

When the converter operates in continuous mode, the current through the inductor \( I_L \) never falls to zero. In the case of an ideal converter operating in steady conditions, the output voltage can be calculated as follows [7]:

During the On-state, the switch “S” is closed, which let the input voltage \( V_{in} \) appearing across the inductor and causes a change in current \( I_L \) flowing through the inductor during a time period \( t \) by the expression:

\[
\frac{\Delta I_L}{\Delta t} = \frac{V_{in}}{L}.
\]

(12)

At the end of the On-state, the increase of \( I_L \) is therefore:

\[
\Delta I_{L_{on}} = \frac{1}{L} \int_0^{D_T} V_{in} \, dt = \frac{D_T}{L} V_{in},
\]

(13)

where \( D \) is the duty cycle; it represents the fraction of the commutation period \( T \) during which the switch is on. Therefore \( D \) ranges between 0 and 1.

During the Off-state, the switch “S” is open, so the inductor current flows through the load. If a zero voltage drop in the diode and a capacitor large enough for its voltage to remain constant are considered, then the evolution of \( I_L \) is:

\[
V_{in} - V_{out} = L \frac{dI_L}{dt}.
\]

(14)

Therefore, the variation of \( I_L \) during the Off-period is:

\[
D I_{L_{off}} = \int_0^T \frac{V_{in} - V_{out}}{L} \, dt = \frac{(V_{in} - V_{out})(1 - D)T}{L}.
\]

(15)
As the converter operates in steady-state conditions, the amount of energy stored in each of its components has to be the same at the beginning and at the end of a commutation cycle. In particular, the energy stored in the inductor is given by:

$$E = \frac{1}{2} L I_L^2.$$  \hfill (16)

At the start and the end of the commutation cycle, the inductor current has to be the same. This means the overall change in the current is zero:

$$\Delta I_{on} + \Delta I_{off} = 0.$$  \hfill (17)

Substituting $\Delta I_{on}$ and $\Delta I_{off}$ by their expressions yields:

$$\Delta I_{on} + \Delta I_{off} = \frac{V_{in} DT}{L} + \frac{(V_{in} - V_{out})(1-D)T}{L} = 0.$$  \hfill (18)

This can be written as:

$$\frac{V_{out}}{V_{in}} = \frac{1}{1-D}.$$  \hfill (19)

In turn, this reveals the duty cycle to be:

$$D = 1 - \frac{V_{in}}{V_{out}},$$  \hfill (20)

(where: $0 \leq D \leq 1$), it has:

$$V_{out} = \frac{1}{C} \int J_{out} \, dt.$$  \hfill (21)

This after derivation becomes:

$$\frac{dV_{out}}{dt} = \frac{1}{C} \left( I_L - \frac{V_{out}}{R} \right) = \frac{1}{C} \left[ I_L - \frac{1}{R} \left( \frac{V_{in}}{1-D} \right) \right],$$  \hfill (22)

where $L$, $R$ and $C$ are respectively the inductor, the resistor and the capacitor of the DC/DC converter, $V_{in}$ and $V_{out}$ are respectively the input voltage and the output voltage of the DC/DC converter.

The efficiency $\eta_{DC/DC}$ of the boost DC/DC converter is given by:

$$\eta_{DC/DC} = \frac{V_{out} \times I_{out}}{V_{in} \times I_{L}} = \frac{P_{out}}{P_{in}}.$$  \hfill (23)

### 2.2.2 MPPT control

The boost (step-up) DC/DC converter is modelled as a block where inputs are the voltage delivered by the PV array and the current generated by the MPPT controller. This MPPT is based on a search algorithm called perturb and observe (P&O) [8].
2.3 Model of the NPC inverter

As shown in Fig. 1 the NPC inverter is formed by three legs. Each leg has four IGBTs connected in series and completed by the addition of two clamp diodes. The bus voltage is split in two by the connection of equal series connected bus capacitors. The NPC inverter can produce three voltage levels on the output: the DC bus plus voltage, zero voltage and DC bus negative voltage. Therefore the total number of IGBTs is twelve that needs twelve signals controls which can be generated by a PWM controller.

2.4 PWM control of the NPC inverter

PWM is a commonly used technique for controlling inverters. It is based on the comparison of two signals. In this paper a triangular wave with two reference sinusoidal waves (the sinusoidal wave and its opposite) is considered. In addition, a closed loop control is performed using a PID controller for each phase. This controller was chosen with coefficients $K_p = 0.0221$, $K_i = 0.0315$ and $K_d = 0$. The control circuit is sketched in Fig. 5.

![PWM control circuit](image)

Fig. 5 – PWM control of the twelve IGBTs.
3 Simulation of the Global System

The simulation diagram of the overall system is shown in Fig. 6. At the entrance of the diagram, a manual switch selects one of these inputs to pass through the output. The intensity of solar radiation (variable or constant) is chosen. Consequently, the output of the switch is connected to the block diagram of the solar module which in turn feeds a DC/DC converter, controlled by the current $I_{\text{ref}}$ generated by an MPPT controller:

$$I_{\text{ref}} = I_{\text{MPP}} \times N_P,$$

where: $N_P$ is the number of branches in parallel and $I_{\text{MPP}}$ the value of current at the maximum power point.

At the output of the DC/DC, an inverter is inserted in the circuit. This is controlled by a strategy based on the PWM which uses a positive triangular signal compared with two reference signals (the sinusoidal signal and its opposite). Outputs of the inverter are connected to the grid and a RL load through an LCL filter. In order to obtain the desired input voltage of the DC/DC converter, 19 modules in series are chosen.

3.1 Simulation results with a constant solar radiation

The scheme of the global grid connected photovoltaic system is given in Fig. 6. As input, the solar radiation of 1000 W/m² is considered.

3.1.1 Simulation results before the inverter

Areas of low voltage are of two types:
- The low-voltage grid “A” where $50 \text{ V} < V_{\text{grid}} \leq 500 \text{ V}$.
- The low-voltage grid “B” where $500 \text{ V} < V_{\text{grid}} \leq 1000 \text{ V}$.

Simulation results corresponding to the second type are presented.
Multiple Low Frequency Dual Reference PWM Control of a Grid Connected…

Fig. 7 – Characteristic of the ENIESOLAR Photovoltaic Generator at the STC conditions.

Fig. 8 – DC/DC boost converter parameters:
(a) Input and output voltage; (b) Input and output power.

From Fig. 8a the output voltage is amplified by the DC/DC converter. This input voltage which was equal to: $V_{PV} = N_S \times V_{MPP} = 19 \times 17.3 = 328.7$ V becomes equal to 1314.8 V ((19) gives $D = 0.75$). Input and output powers of the DC/DC converter are presented in Fig. 8b. From both, Fig. 8b and calculation, an efficiency of 0.938 is found.

3.1.2 Simulation results after the inverter

3.1.2.1 Simulation results with grid disconnected for constant solar radiation

The simulation results are presented in Figs. 9, 10 and 11.

At the beginning of the MPPT algorithm, i.e. at $t = 0$, the power $P_{PV}$ is equal zero which explains the operation in open circuit ($V_{PV} = V_{OC} = 410.4$ V). After a sampling time which is considered $< 0.01$, the voltage $V_{PV}$ drops to the value $V_{MPP} = 328.7$ V.

The inverter output voltage (before the filter) is shown in Figs. 9, 10 and 11. It is a PWM signal where the carrier wave frequency is equal to 3 kHz (which is low) and the same for the currents. At the beginning of the simulation the inverter is powered by the capacitor initially charged. Once the capacitor is discharged, the inverter is powered by the photovoltaic source.
Figs. 12a, 13a and 14a illustrate the current before and after the filter in each phase. A transitional regime is observed which ends a period of 0.02 s where the current is very large. This can be explained by the fact that the grid sends the current to the system. This leads to use a filter with a coil having a resistor of a few ohms.

**Fig. 9 – Inverter output parameters for the phase “a” (with grid disconnected):**
(a) Output voltage “V_a” before and after the filter;
(b) Output current “I_a” before and after the filter.

**Fig. 10 – Inverter output parameters for the phase “b” (with grid disconnected):**
(a) Output voltage “V_b” before and after the filter;
(b) Output current “I_b” before and after the filter.

**Fig. 11 – Inverter output parameters for the phase “c” (with grid disconnected):**
(a) Output voltage “V_c” before and after the filter;
(b) Output current “I_c” before and after the filter.
From $t = 0.025$ s, the system is powered by the photovoltaic source. Therefore the output current of the inverter supplies the load and the remaining current is injected into the grid. Figs. 12b, 13b and 14b show waveforms of various currents.

### 3.1.2.2 Simulation results with grid connected for constant solar radiation

When the grid is connected, the following simulation results are obtained (see Figs. 12 – 15).

**Fig. 12** – Inverter output current “$I_a$” (with grid connected):
(a) Inverter output current “$I_a$” before and after the filter;
(b) Inverter output currents “$I_{af}$”, $I_{a-Load}$ and $I_{a-grid}$.

**Fig. 13** – Inverter output current “$I_b$” (with grid connected):
(a) Inverter output current “$I_b$” before and after the filter;
(b) Inverter output currents “$I_{bf}$”, $I_{b-Load}$ and $I_{b-grid}$.

**Fig. 14** – Inverter output current “$I_c$” (with grid connected):
(a) Inverter output current “$I_c$” before and after the filter;
(b) Inverter output currents “$I_{cf}$”, $I_{c-Load}$ and $I_{c-grid}$.
3.1.2.3 Efficiency of the inverter for constant solar radiation

To calculate the efficiency the average power output of the inverter, given by the following expression, must be determined:

\[
P_{\text{out-inv}} = \frac{1}{T} \int_0^T P_{\text{out-inv}}(t) \, dt = \frac{1}{T} \int_0^T [p_a(t) + p_b(t) + p_c(t)] \, dt,
\]

where \( P_{\text{out-inv}}(t) \) is the sum of the instantaneous powers of the three phases (which are respectively \( p_a(t) \), \( p_b(t) \) and \( p_c(t) \)), with

\[
p_a(t) = v_a(t) \times i_a(t) = V_{\text{max}} \sin(\omega t - \varphi) I_{\text{max}} \sin(\omega t),
\]

\[
p_b(t) = v_b(t) \times i_b(t) = V_{\text{max}} \sin\left(\omega t - \varphi - \frac{2\pi}{3}\right) I_{\text{max}} \sin\left(\omega t - \frac{2\pi}{3}\right),
\]

\[
p_c(t) = v_c(t) \times i_c(t) = V_{\text{max}} \sin\left(\omega t - \varphi - \frac{4\pi}{3}\right) I_{\text{max}} \sin\left(\omega t - \frac{4\pi}{3}\right).
\]

Substituting these powers by their expressions in equation (25) yields:

\[
P_{\text{out-inv}} = \frac{1}{T} \int_0^T P_{\text{out-inv}}(t) \, dt = \frac{1}{T} \int_0^T \left[V_{\text{max}} \sin(\omega t - \varphi) I_{\text{max}} \sin(\omega t) + V_{\text{max}} \sin\left(\omega t - \varphi - \frac{2\pi}{3}\right) I_{\text{max}} \sin\left(\omega t - \frac{2\pi}{3}\right) + V_{\text{max}} \sin\left(\omega t - \varphi - \frac{4\pi}{3}\right) I_{\text{max}} \sin\left(\omega t - \frac{4\pi}{3}\right)\right] \, dt,
\]

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where $V_{\text{max}} = \sqrt{2}V_{\text{rms}}$ and $I_{\text{max}} = \sqrt{2}I_{\text{rms}}$, $(V_{\text{rms}}$ and $I_{\text{rms}}$ are respectively the RMS values of the voltage and the current in each phase, $\phi$ is the phase shift between the voltage and the current.

After calculation, the average power output of the inverter becomes:

$$P_{\text{out-inv}} = 3V_{\text{rms}}I_{\text{rms}} \cos(\phi). \quad (30)$$

The inverter efficiency is given by:

$$\eta_{\text{DC/AC}} = \frac{P_{\text{out-inv}}}{P_{\text{DC/DC}}}. \quad (31)$$

The DC/DC converter output power and the average power output of the inverter are presented in Fig. 16.

![DC/DC converter output, inverter output and filter output powers.](image)

**Fig. 16** – *DC/DC converter output, inverter output and filter output powers.*

By using equations (23) and (29), efficiencies of the DC/DC converter and the inverter are presented in Fig. 17.

![DC/DC converter and inverter efficiencies](image)

**Fig. 17** – *Efficiency of the DC/DC converter and the inverter.*

The filter used in this work is an LCL filter because it is very suitable in the case where the PV system is connected to the grid. As it is shown in Fig. 16, the DC/DC converter output power is 13366.5 W and the inverter output power is 12835 W before the filter but the power after the filter is only 11623.5 W. This
is due to the Joule effect losses in the resistances of the filter coils which are taken equal to $1 \, \Omega$ each. These losses can be reduced on one hand by decreasing the values of the coils resistances. On the other hand, by using equations (23) and (29) the efficiencies of the DC/DC converter and the inverter are respectively: $\eta_{\text{DC/DC}} = 0.938$ and $\eta_{\text{DC/AC}} = 0.96$. According to the latest value, the losses in the inverter switches are very low and they can be neglected. These losses represent On-state losses, Off-state losses, Turn-on losses and Turn-off losses and can be calculated from the expressions given in the references [9, 10].

3.2 Simulation Results with a Variable Solar Radiation

Solar radiation is simulated as a curve which starts with low values in the morning and evolves over time to reach a peak value (1000 W/m$^2$) at noon and then decreases in the evening. The number of hours of sunshine is greater or equal to 9 hours i.e. for weather conditions called Type “A” [11]. In this work, the number of hours is taken equal to 15 hours. Knowing that the period of sinusoidal signal equals to 0.02 s (which is very negligible to 24 hours). So a simulation time of $24 \, \text{h} \times 3600 = 86400 \, \text{s}$ divided by 100000 which gives a value of 0.864 s is taken to visualize the behavior of voltages and currents at the inverter output. The simulation time is given to imitate the behavior of a real solar radiation (see Fig. 18).

The simulation results with grid connected are given as follows:

In Fig. 19a, a marked amplification of the converter input voltage is observed. By using (19) the duty cycle of the boost converter $D = 0.75$ is found. The efficiency of the boost converter $\eta = 0.938$ is obtained by using Fig.19b and (23).

![Fig. 18 – Daily variable solar radiation.](image)

Figs. 20a, 21a and 22a show the voltages of the three phases before and after the output filter. All these figures are accompanied with an enlargement at the beginning of the simulation (see Figs. 20b, 21b and 22b), where the inverter is fed by the capacitor.
In Fig. 23, one cycle of the voltages is shown where for each phase voltage before the filter has a rectangular shape, with a peak value of 657.4 V that is virtually equal to the DC/DC converter voltage divided by two. The voltage after the filter is a sine wave with a peak value of 575 V. The difference between the two voltage values is due to the voltage drop across the filter.

![Graph](image1)

![Graph](image2)

**Fig. 19** – DC/DC boost converter parameters:
(a) Input and output voltage; (b) Input and output power.

### 3.2.1 Simulation results with grid disconnected for variable solar radiation

Fig. 24 shows the three phase inverter output currents before the filter, they start with low values then they reach their peak values of 13.5 A at noon and finally drop to zero in the absence of radiation. The sinusoidal shape of the output currents of the inverter, sketched on Fig. 25, is very clear. One cycle of currents is shown in Fig. 26.

![Graph](image3)

![Graph](image4)

**Fig. 20** – Inverter output voltage “$V_a$” before and after the filter (with grid disconnected):
(a) Voltage “$V_a$” for the variable solar radiation;
(b) Enlargement of the voltage “$V_a$” at the beginning of simulation.
Fig. 21 – Inverter output voltage “$V_b$” before and after the filter (with grid disconnected): (a) Voltage “$V_b$” for the variable solar radiation; (b) Enlargement of the voltage “$V_b$” at the beginning of the simulation.

Fig. 22 – Inverter output voltage “$V_c$” before and after the filter (with grid disconnected): (a) Voltage “$V_c$” for the variable solar radiation; (b) Enlargement of the voltage “$V_c$” at the beginning of simulation.

Fig. 23 – Inverter output voltages “$V_a$”, “$V_b$” and “$V_c$” before and after the filter [one cycle is shown]: (a) $V_a$; (b) $V_b$; (c) $V_c$. 

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3.2.2 Simulation results with grid connected for variable solar radiation

Fig. 27 shows the voltages of three phases, before and after the filter, at the output of the inverter. Before the filter, it is observed that the voltages start with a small value (equal to that of the capacitor divided by 2 \( V_{dc}/2 = 200/2 = 100 \text{ V} \)), then they increase more and more up to \( t = 0.26 \text{ s} \) where the photovoltaic source starts to charge the system. The corresponding currents (Fig. 28) start with a
high value (around 65 A) which lasts for a period of 0.02 s (33 minutes in reality) at the value of 13.5 A. Inverter output currents for each phase are presented in Fig. 29, wherein at \( t = 0 \) a transition period where the current imposed by the grid is clearly equal the sum of the current supplied to the system and that which supplies the load. But when solar radiation reaches a high enough value the photovoltaic system supplies the load and sends the remaining current in the grid, then it tends to zero in the evening because the radiation is low, and the load is fed back via the grid.

Fig. 27 – Inverter output voltage before and after the filter with grid connected: (a) Voltage “\( V_a \)” for phase “a”; (b) Voltage “\( V_b \)” for phase “b”; (c) Voltage “\( V_c \)” for phase “c”.

Fig. 28 – Inverter output current after the filter (with grid connected): (a) Inverter output current “\( I_a \)” before and after the filter; (b) Inverter output current “\( I_b \)” before and after the filter;
Fig. 28c – Inverter output current after the filter (with grid connected):
Inverter output current “Ic” before and after the filter.

Fig. 29 – Inverter output current (with grid connected):
(a) Inverter output currents “Iaf”, Ia-Load and Ia-grid;
(b) Inverter output currents “Ibf”, Ib-Load and Ib-grid;
(c) Inverter output currents “Icf”, Ic-Load and Ic-grid.

3.2.3 Efficiency of the inverter for variable solar radiation

Fig. 30 illustrates the input and the output power of the inverter (before and after the filter). The output power of the inverter before the filter is almost equal to the input power, giving efficiency equal to 0.96. This shows that the switching and the conduction losses in the switches of the inverter are negligible. Therefore, the power after the filter is significantly lower than the input power, which gives efficiency equals to 0.9. So, it is very clear that the Joule effect losses in the filter are responsible for this decrease.
3.2.4 Fast Fourier Transform (FFT) analysis

The Fast Fourier Transform (FFT) analysis, using the POWERGUI block, yields to the following results:

### Table 1

*The THD (%) of the voltage and the current of phase “a” for three values of “$f_c$”.*

<table>
<thead>
<tr>
<th>Phase “a”</th>
<th>THD (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>$f_c = 50$Hz</td>
</tr>
<tr>
<td>$I_{anf}$</td>
<td>64.49</td>
</tr>
<tr>
<td>$I_{af}$</td>
<td>42.45</td>
</tr>
<tr>
<td>$V_{anf}$</td>
<td>37.43</td>
</tr>
<tr>
<td>$V_{af}$</td>
<td>44.01</td>
</tr>
</tbody>
</table>

In the Table 1, the Total Harmonic Distortion (THD) is given for phase “a” for three switching frequencies. The THD decrease with increasing frequency is observed. The best results are obtained for the frequency $f_c = 3$ kHz, where: $f_c$ is the carrier wave frequency in PWM control. $I_{anf}$ and $V_{anf}$ are respectively the...
unfiltered current and voltage of the phase “a”, $I_{af}$ and $V_{af}$ are respectively the filtered current and voltage of the phase “a”. The magnitude of the fundamental harmonic and the THD value are given by Fig. 32.

![Fig. 32 – Spectrum of the inverter output voltage and current after the filter for phase “a”:](image)

(a) Spectrum of the unfiltered inverter output voltage $V_{anf}$.
(b) Spectrum of the unfiltered inverter output current $I_{anf}$.
(c) Spectrum of the filtered inverter output voltage $V_{af}$.
(d) Spectrum of the filtered inverter output voltage $I_{af}$.

In the case of a constant solar radiation (Fig. 32) it is observed that the response of the DC/DC converter and the inverter is very fast, i.e. at $t$ less than 0.01 s all parameters (voltages and currents), appear at inputs and outputs of the two converters. Therefore, in the case of variable solar radiation, the response of the DC/DC boost converter and the inverter is slow. All parameters (voltage and current) at the inputs and outputs of the converters appear at $t = 0.26\ s$, the response is zero at low values of solar radiation, which means that the DC/DC boost converter and the inverter operate from $t = 0.26\ s$ to $t = 0.60\ s$, i.e. from 7:25am to 04:40pm. The operating real time is 10 hours and 45 minutes.

4 Conclusion

In this paper the simulation study in Matlab/Simulink of a grid connected photovoltaic three-phase NPC inverter with DC/DC boost converter has been presented. The control of this inverter is based on a PWM strategy which uses a carrier wave with low frequency equal to 3 kHz and two reference signals. A MPPT controller (with P&O algorithm) is used to generate current control of the DC/DC boost converter. The inverter is simulated for two patterns of solar radiation.
radiation, one is constant and the other is variable. The obtained results are satisfactory despite the low value of the frequency of the carrier wave PWM control strategy. The only drawback is the presence of Joule effect losses in the filter which reduce slightly the efficiency of the inverter. On the other hand, the use of an open-loop control has an effect on the amplitude of the sinusoidal voltage at the output of the inverter. But, this problem is overcome with the use of a current closed-loop control based on the addition of PID controllers.

When the PV system is connected to the grid it is necessary to use an LCL filter with coil resistance of a few ohms. Also a multi-level inverter can be used where the output voltage is with a number of levels greater than three.

The THD obtained for the voltages and the currents are very low (< 1.0%) which are good results. The advantage of the inverter is, on the one hand, the series connection of IGBTs will increase the voltage at its input without fear of overvoltage. On the other hand, other IGBTs can be added to the inverter in order to be connecting to the medium voltage grid.

5 Parameters of ENIESOLAR module

“ENIESOLAR” is a solar module manufactured by the National Company Electronic Industries in Algeria, which characteristics are given as follow:

<table>
<thead>
<tr>
<th>Parameter</th>
<th>value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximal power</td>
<td>75 W, ± 10%</td>
</tr>
<tr>
<td>Short circuit current $I_{SC}$</td>
<td>4.67 A</td>
</tr>
<tr>
<td>Open circuit voltage $V_{OC}$</td>
<td>21.6 V</td>
</tr>
<tr>
<td>MPP voltage $V_{MPP}$</td>
<td>17.30 V</td>
</tr>
<tr>
<td>MPP current $I_{MPP}$</td>
<td>4.34 A</td>
</tr>
<tr>
<td>minimum value of the fuse in series</td>
<td>10 A</td>
</tr>
<tr>
<td>number of cells in series</td>
<td>36</td>
</tr>
<tr>
<td>number of cells in parallel</td>
<td>1</td>
</tr>
</tbody>
</table>

5 References


